

John S. Connors

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Education

M.S. in Computer Engineering, September 2007
University of California at Santa Cruz, Santa Cruz CA
Autonomous Systems Laboratory - Prof. Gabriel Elkaim

B.A. in Computer Science and Mathematics, December 2004
University of California at Berkeley, Berkeley CA

Experience

Schematic and Circuit Design
PCB Layout and Fabrication
Xilinx and Altera FPGA Digital Design
Embedded Microcontroller Design
Digital System Design and Debugging
Prototype Fabrication and Integration
Design and Construction of Custom Electric Vehicles
Programming: Verilog, Java, C/C++, MIPS/x86 Assembly
Electro-Mechanical Integration
High Voltage DC Power Circuits
System Integration
Field and Endurance Testing

Projects

Path Planning and Obstacle Avoidance
Autonomous Vehicle Control
Hardware Division Unit for SCOORE Processor
Electric and Solar Powered Vehicles
Data Logging and Telemetry System for Electric Vehicle
Audio/Video Distribution Network for Instructional Lab
User Interface System for Calinx Development Board
Set Associative Data Cache for MIPS Processor
Video Effects Processor with Ethernet Interface

Employment

Engineering Internship, April 2007 - Present

Wrightspeed Inc.

Modification of prototype electric supercar and business development

Graduate Student Researcher, September 2005 - September 2007

University of California, Santa Cruz

Autonomous vehicle research, path planning, vehicle control and design

Electrical Engineer, September 2002 - July 2005

CalSol - UC Berkeley Solar Vehicle Team

Design, manufacturing and testing of electrical systems, schematic design, PCB layout, project management, software integration